

Notice of References Cited

Application/Control No.

09/872,590

Applicant(s)/Patent Under
Reexamination
SRIVASTAVA ET AL.

Examiner

Satish S. Rampuria

Art Unit

2191

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0133806 A1	09-2002	Flanagan et al.	717/123
*	B	US-2004/0205718 A1	10-2004	Reynders, John V.W.	717/124
*	C	US-5,537,319 A	07-1996	Schoen, Eric J.	702/14
*	D	US-6,862,711 B1	03-2005	Bahrs et al.	715/205
*	E	US-7,251,809 B2	07-2007	Barclay et al.	717/128
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	A software approach to multiprocessor address trace generation, Azimi, M.; Erickson, C., Computer Software and Applications Conference, 1990. COMPSAC 90. Proceedings., Fourteenth Annual International, 31 Oct-2 Nov 1990, IEEE, □□Page(s): 99-105
	V	Trace-driven and program-driven simulation: a comparison, Malloy, B.A., Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 1994., MASCOTS '94., Proceedings of the Second International Workshop on, 31 Jan-2 Feb 1994, IEEE, Page(s): 395-396
	W	SIMPLE: a universal tool box for event trace analysis, Dauphin, P.; Hofmann, R.; Lemmen, F.; Mohr, B., □□Computer Performance and Dependability Symposium, 1996., Proceedings of IEEE International, 4-6 Sep 1996, Page(s): 59-59 □□
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.